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AM62Q x8NAND CARD - REVA

ECR/ECN DOCUMENT

Document Info

Document Name	MS_TI_AM62Q_x8NAND_CARD_ECR_ECN.doc		
Document Number	0.2	Date	30-09-2022
Written By	Deepak NS, Mark M	Reviewed By	Krishna Prasad
Distribution	Assembly Team		

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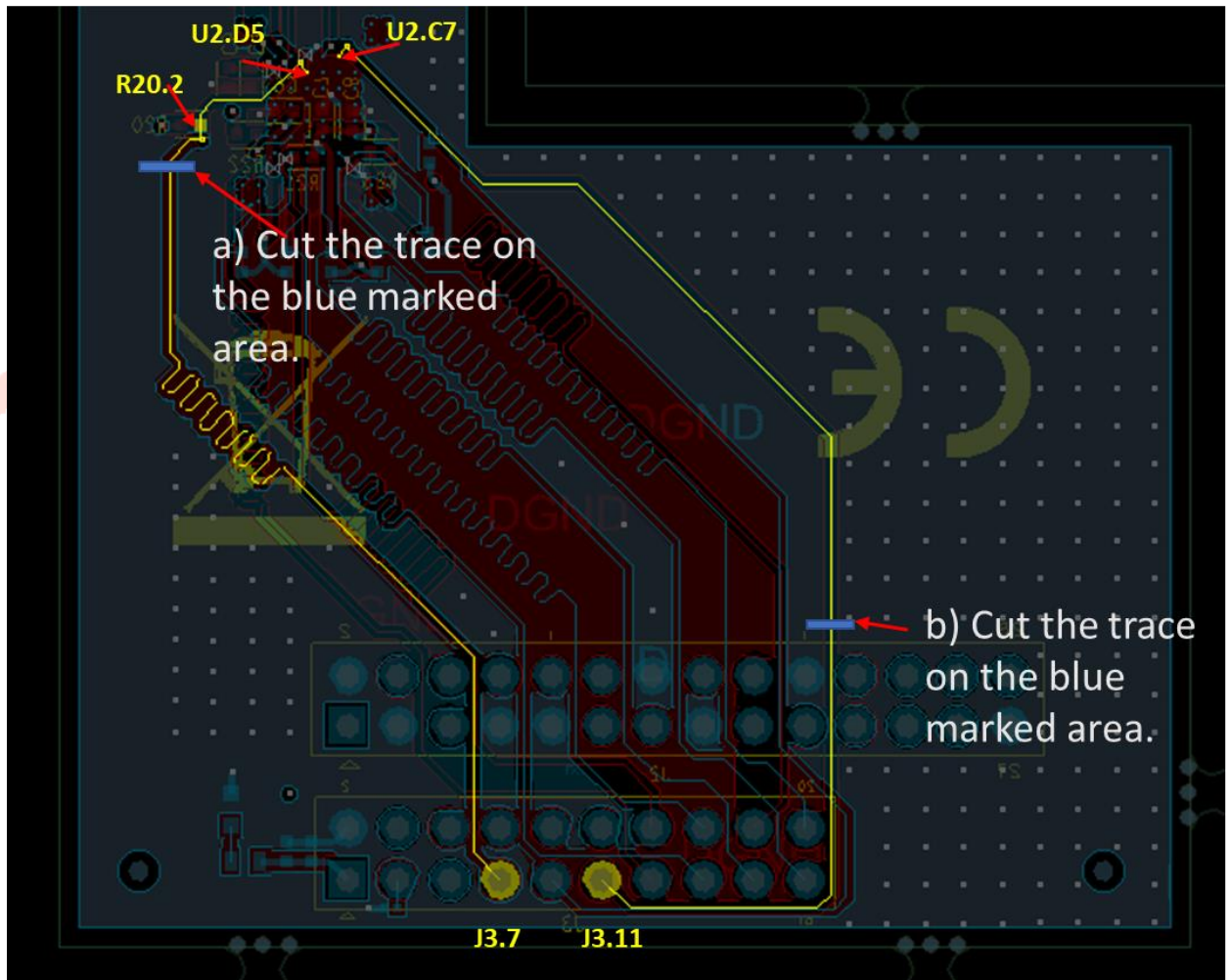
Revision History

Revision Number	Date	Nature of Revision	Changed by	Approved by
0.1	14-09-2022	1. CLE and WE are swapped 2. ALE is routed to the wrong pin	Deepak ns	Krishna Prasad
0.2	30-09-2022	3. VCC3V3_PRU is off by default, wire power from VCC_3V3_SYS	Mark M	Mark M

Project Name	Document Name	Document Number	REV
AM62Q x8NAND CARD	ECR_ECN	MS_TI_AM62Q_x8NAND_CARD_ECR_ECN_REVA	0.2

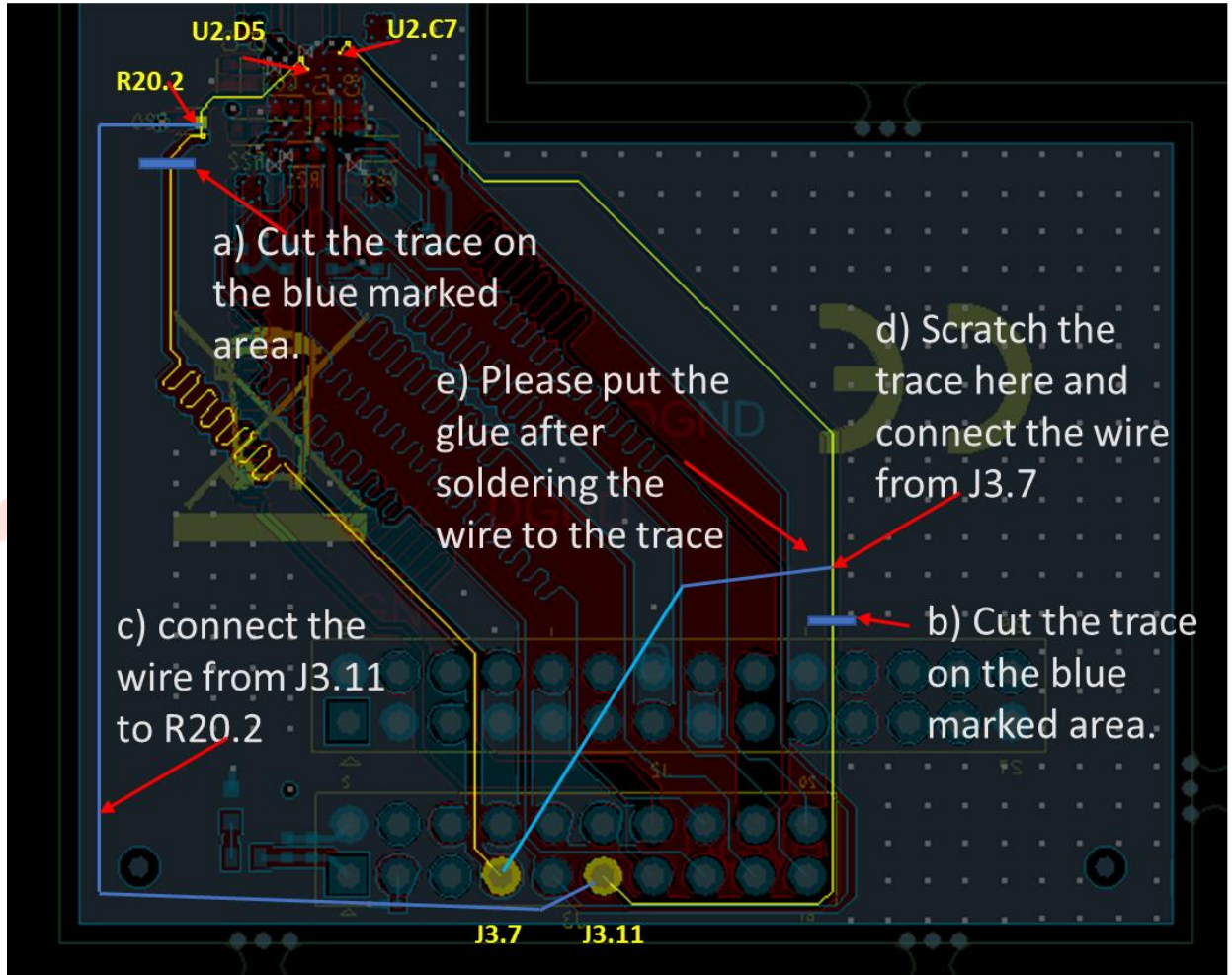
BRIEF DESCRIPTION OF THE CHANGE TO BE IMPLEMENTED

ECR#	ECR Description	ECN	Remarks
1	CLE and WE signals are swapped.	a) Cut the trace between J3.7 to R20.2. b) Cut the trace between J3.11 to U2.C7	

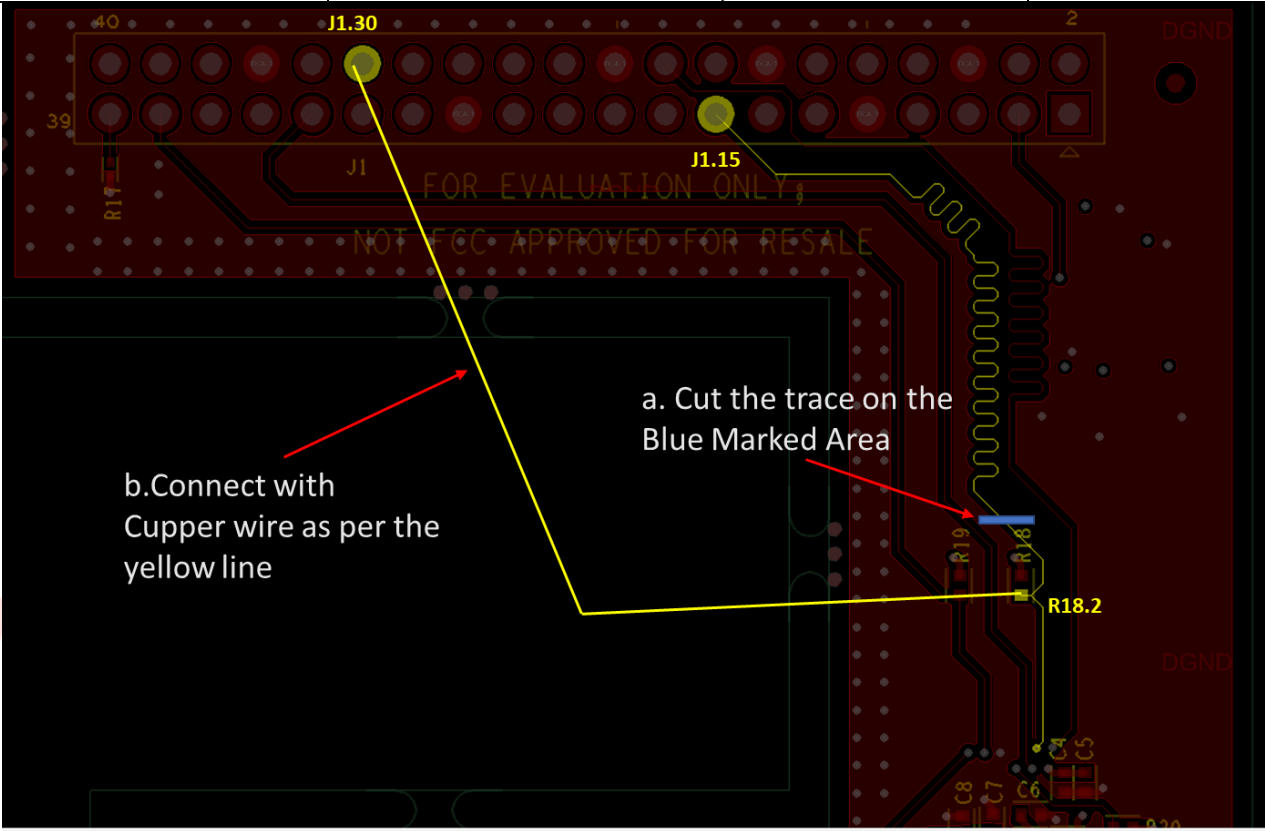


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- c) Connect R20.2 to J3.11 with copper wire to route CLE to GPMC0_BE0N_CLE on PRU HEADER pin 11.
- d) Connect U2.C7 trace to J3.7 with copper wire to route WE# to GPMC0_WEN on PRU HEADER pin 7.
- e) Put the glue after soldering the wire to the trace.



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2	ALE is routed to the wrong pin	a) Cut the trace between R18.2 to J1.15. b) Connect R18.2 to J1.30 with copper wire to route ALE to GPMC0_ADVn_ALE on USER EXPANSION HEADER pin 30	
			

ECR#	ECR Description	ECN	Remarks
3	VCC3V3_PRU is off by default, wire power from VCC_3V3_SYS	VCC3V3_PRU is off by default at U65 on the AM62Q LP SK, and there is no way to turn it on until after SW is running to configure IO expander. Workaround: Add a jumper wire from NAND board J2.1 to J3.1 – this routes VCC_3V3_SYS from MCU HEADER pin 1 to VCC_3V3_NAND (VCC3V3_PRU) which is off.	

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